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CERTIFICATE OF MAILING

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Z. R. Watt
Attorney for Applicants

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.	:	10/736,238	Confirmation No.: 3151
Applicant	:	Hua-Jan Lo et al.	
Title	:	LOW VOLTAGE DIFFERENTIAL SIGNALING DEVICE WITH FEEDBACK COMPENSATION	
Filed	:	December 12, 2003	
TC/A.U.	:	(not yet assigned)	
Examiner	:	(not yet assigned)	
Docket No.	:	3304.2.105	
Customer No.	:	21552	

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Transmitted herewith is an Information Disclosure Statement disclosing information which has come to the attention of applicants and/or their attorneys and is being submitted so as to comply with the duty of disclosure set forth in 37 C.F.R. § 1.56. In accordance with 37 C.F.R. § 1.97(b), the enclosed Statement is being filed within three (3) months of the filing date of the above-identified application or before the mailing date of a first Action on the merits.

Neither applicants nor their attorneys make any representation that any information disclosed herein may be "prior art" within the meaning of that term under 35 U.S.C. § 102 or § 103. Moreover, pursuant to 37 C.F.R. § 1.97, the filing of this Information Disclosure Statement

shall not be construed as a representation that a search has been made or as an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

In accordance with 37 C.F.R. § 1.98, transmitted herewith are:

1. A completed copy of Form(s) PTO/SB/08a and/or PTO/SB08b "Information Disclosure Statement by Applicant" listing the patents, publications and other information being submitted for consideration; and
2. A legible copy of each patent, publication and other item of information in written form listed on the enclosed Forms PTO/SB/08a and PTO/SB/08b, except for copies of U.S. patents and published U.S. patent applications which are not required for applications filed after June 30, 2003.

As this application was filed after June 30, 2003, copies of the U.S. patents and published U.S. patent applications listed on the enclosed Form PTO/SB/08a are not required and, therefore, not included herewith.

NON-ENGLISH INFORMATION

Pursuant to 37 C.F.R. § 1.98, following is a concise explanation of the relevance (as it is presently understood by the individual designated in 37 C.F.R. § 1.56(c) most knowledgeable about the content of the information), of each listed patent, publication or other information that is not in the English language:

1. Taiwan Publication discloses: High-speed LVDS I/O interface cells are important circuits to diminish the difference of speed between the inside and the outside of current CMOS chips. In this paper, a few basic circuits of LVDS I/O interface cells and how they work are discussed in detail, and the results of simulation using HSPICE tools are also presented.

Respectfully submitted,



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Date: June 14, 2004

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PTO/SB/08a (08-03)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 Of 1

Complete if Known

Application Number	10/736,238
Filing Date	December 12, 2003
First Named Inventor	Hua-Jan Lo
Group Art Unit	
Examiner Name	
Attorney Docket Number	3304.2.105

U.S. PATENT DOCUMENTS

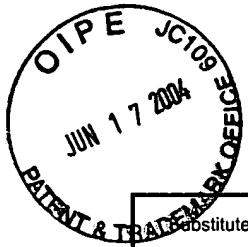
Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (<i>If known</i>)			
U1		US-6,025,792-B1	02/15/2000	Smith	

Examiner Signature	Date Considered
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¹EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ²Applicant's unique citation designation number (optional). ³See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ⁴Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁵For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁶Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁸Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 Of 1

Complete if Known

Application Number	10/736,238
Filing Date	December 12, 2003
First Named Inventor	Hua-Jan Lo
Group Art Unit	
Examiner Name	
Attorney Docket Number	3304.2.105

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
O1	Liu, Xiang-yuan, et al., "The Study and Design of High-Speed LVDS I/O Interface Cells," Computer Engineering & Science, Vol. 23, No. 4, 2001. page 52-57.		

Examiner Signature	Date Considered
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¹EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

²Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached.

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